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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/925,952	08/10/2001	Tetsuo Endoh	900-397	5574

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NIXON & VANDERHYE, PC  
1100 N GLEBE ROAD  
8TH FLOOR  
ARLINGTON, VA 22201-4714

EXAMINER

NGUYEN, JOSEPH H

ART UNIT	PAPER NUMBER
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2815

DATE MAILED: 02/05/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

**Advisory Action**

Application No.

09/925,952

Applicant(s)

ENDOH ET AL.

Examiner

Joseph Nguyen

Art Unit

2815

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 03 December 2003 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

**PERIOD FOR REPLY** [check either a) or b)]

- a) ☒ The period for reply expires 4 months from the mailing date of the final rejection.
- b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.
- ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2. ☐ The proposed amendment(s) will not be entered because:
- (a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);
- (b) ☐ they raise the issue of new matter (see Note below);
- (c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
- (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_

3. ☐ Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.
4. ☐ Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5. ☒ The a) ☐ affidavit, b) ☐ exhibit, or c) ☒ request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7. ☒ For purposes of Appeal, the proposed amendment(s) a) ☐ will not be entered or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_\_

Claim(s) objected to: \_\_\_\_\_

Claim(s) rejected: 1, 3, 5, 7-9, 19, 29, 31-46, 49-52 and 111.Claim(s) withdrawn from consideration: 2, 4, 6, 10, 20-28, 30, 47-48.

8. ☐ The drawing correction filed on \_\_\_\_\_ is a) ☐ approved or b) ☐ disapproved by the Examiner.
9. ☒ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). \_\_\_\_\_.
10. ☒ Other: See Continuation Sheet

Continuation of 5. does NOT place the application in condition for allowance because: Applicant argues that Burns et al fails to disclose or suggest "the depletion layer" recited in claim 1 for electrically insulating the active region of the memory cell from the semiconductor substrate. However, Burns et al discloses on figure 10 a pn junction between the second conductivity type diffusion layer 215 in the substrate 235. Note that substrate 235 can be p doped (col. 8, lines 24-26). This implies that there exists a depletion layer at this junction. Further, with respect to claim 19, Burns et al also teaches that a lower gate electrode of a selection transistor, the control gate of the memory cell and an upper gate electrode of another selection transistor are arranged in an upward order in a direction vertical to the semiconductor substrate .

Continuation of 10. Other: Acknowledgement is made of a claim for foreign priority under 35 U.S.C 119 (a) in this instant application.

  
JEROME JACKSON  
PRIMARY EXAMINER